

RIFF JTAG Error codes and troubleshooting

Error: Wrong DCC Data Checksum

2. *Wrong DCC Data Checksum*

Happens when DCC Loader which is being executed by target is too slow to respond through the DCC channel. Since RIFF BOX's firmware in normal state never checks DCC channel RX and TX ready flags (such checks slow down the communication speed and were decided to be omitted) sometimes it happen DCC Loader supplies data to DCC channel slower than BOX's firmware reads from it. Thus trash is received.

Solution:

User shall lower TCK/RTCK Sampling speed settings in order to allow more slow communication between DCC Loader and host.

RTCK Settings and TCK settings:

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JTAG TCK Speed:

RTCK

RTCK

5 kHz

100 kHz

200 kHz

400 kHz

800 kHz

1 MHz

2 MHz

3 MHz

4 MHz

6 MHz

8 MHz

9 MHz

10 MHz

12 MHz

14.4 MHz

18 MHz

RESET, Wait 0 ms, Special

JTAG I/O Voltage:

1.80V

TAP # (Multichain position):

0

JTAG TCK Speed:

RTCK

Sample at MAX

Sample at 20 kHz

Sample at 40 kHz

Sample at 60 kHz

Sample at 80 kHz

Sample at 100 kHz

Sample at 200 kHz

Sample at 400 kHz

Sample at 800 kHz

Sample at 1 MHz

Sample at 2 MHz

Sample at 3 MHz

Sample at 4 MHz

Sample at 6 MHz

Sample at 8 MHz

Sample at 9 MHz

Sample at 10 MHz

Sample at 12 MHz

1.80V

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