RIFF JTAG Error codes and troubleshooting

Error code 0x2B

2.7. Error code 0x2B

Returned by the DCC Loader when it attempted to erase block of flash memory and fails waiting for selected flash memory controller to assert ready status.

Explanation:

May happen if target is currently in some unrecoverable h/w state or if memory controller is not properly initialized or due to some other reasons. Shall not happen since it is DCC Loader’s task to initialize and communicate with memory controller properly. Can happen if target has a damaged memory chip.

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URL: https://faq.riffbox.org/content/4/32/en/error-code-0x2b.html